WEST Search History

DATE: Tuesday, April 29, 2003

Set Name side by side		Hit Count	Set Name result set	
DB=PC				
LII	L10 and (thin\$3 or grind\$3 or remov\$3)	107	Lll	
L10	L9 and ((bump or projection) ADJ (electrode or terminal))	121	L10	
L9	stacked and chip	4384	L9	
L8	L7 and ((bump or projection) ADJ (electrode or terminal))	8	L8	
L7	chip-on-chip	70	L7	
DB=USPT; PLUR=YES; OP=ADJ				
L6	L5 and (rear or backside or (back ADJ side)) SAME (thin\$4 or grind\$3 or remov\$3)	21	L6	
L5	L4 and (thin\$4 or grind\$3 or remov\$3)	173	L5	
L4	L3 and ((bump or projection) ADJ (electrode or terminal))	186	L4	
L3	stacked and chip	16536	L3	
L2	L1 and ((bump or projection) ADJ (electrode or terminal))	6	L2	
L1	chip-on-chip	129	L1	

END OF SEARCH HISTORY

WEST Search History

DATE: Tuesday, April 29, 2003

Set Nam side by sid	<u>de</u> <u>Ouery</u> de	Hit Count	Set Name result set		
DB=USPT,PGPB,JPAB,EPAB,DWPI; PLUR=YES; OP=ADJ					
L18	L17 and ((rear or backside or (back ADJ side)) SAME (thin\$3 or grind\$3 or remov\$3))	204	L18		
L17	L16 and ((bump or projection) ADJ (electrode or terminal))	5864	L17		
L16	semiconductor or semiconductive or silicon	1664126	L16		
DB=USPT; $PLUR=YES$; $OP=ADJ$					
L15	6256207-pn.	1	L15		
L14	6084295.pn.	1	L14		
L13	5737191.pn.	1	L13		
L12	5602059.pn.	1	L12		
DB=PGPB; $PLUR=YES$; $OP=ADJ$					
LII	L10 and (thin\$3 or grind\$3 or remov\$3)	107	L11		
L10	L9 and ((bump or projection) ADJ (electrode or terminal))	121	L10		
L9	stacked and chip	4384	L9		
L8	L7 and ((bump or projection) ADJ (electrode or terminal))	8	L8		
L7	chip-on-chip	70	L7		
DB=USPT; $PLUR=YES$; $OP=ADJ$					
L6	L5 and (rear or backside or (back ADJ side)) SAME (thin\$4 or grind\$3 or remov\$3)	21	L6		
L5	L4 and (thin\$4 or grind\$3 or remov\$3)	173	L5		
L4	L3 and ((bump or projection) ADJ (electrode or terminal))	186	L4		
L3	stacked and chip	16536	L3		
L2	L1 and ((bump or projection) ADJ (electrode or terminal))	6	L2		
Ll	chip-on-chip	129	L1		

END OF SEARCH HISTORY